

Inventor: SHANG-CHIH CHEN
Serial No.: To Be Assigned Filed: Herewith
For: HIGH-K GATE DIELECTRIC STACK WITH BUFFER LAYER
TO IMPROVE THRESHOLD VOLTAGE CHARACTERISTICS
Attorney Doc. No.: 67,200-1258

1/2

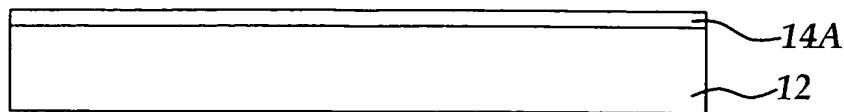


Figure 1A

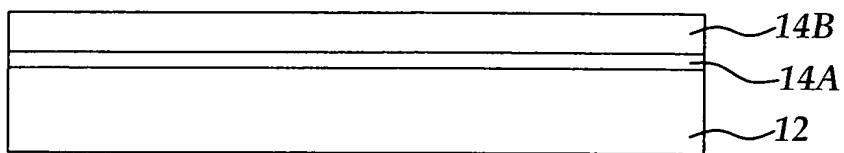


Figure 1B

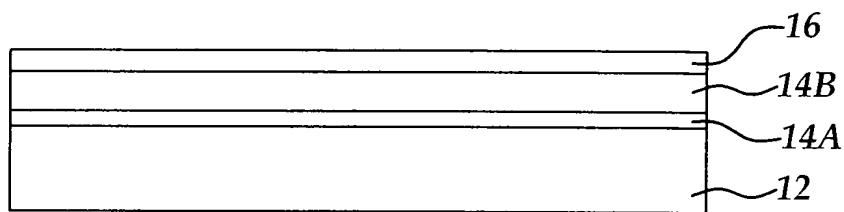


Figure 1C

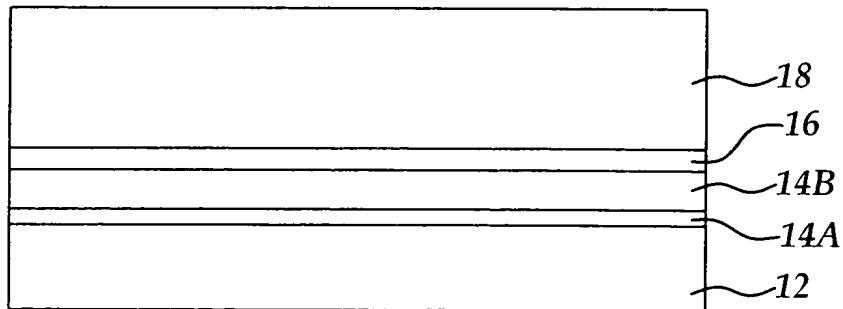


Figure 1D

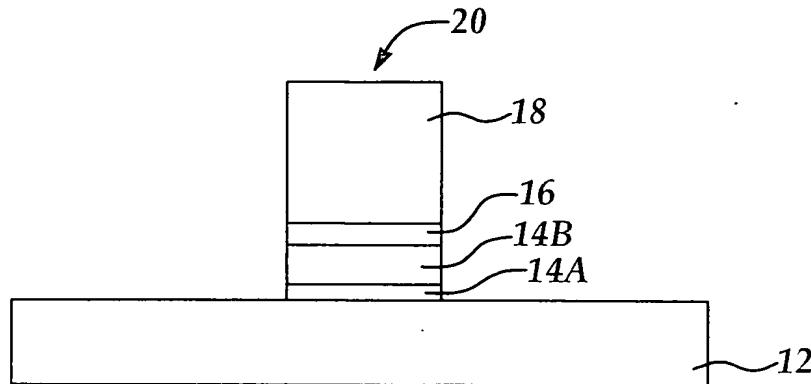


Figure 1E

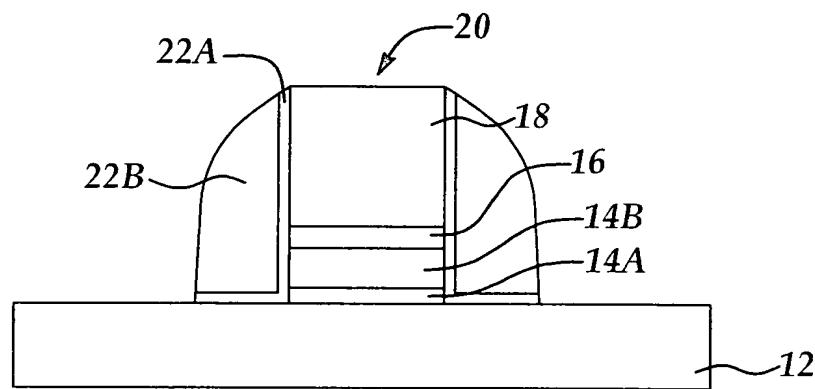


Figure 1F

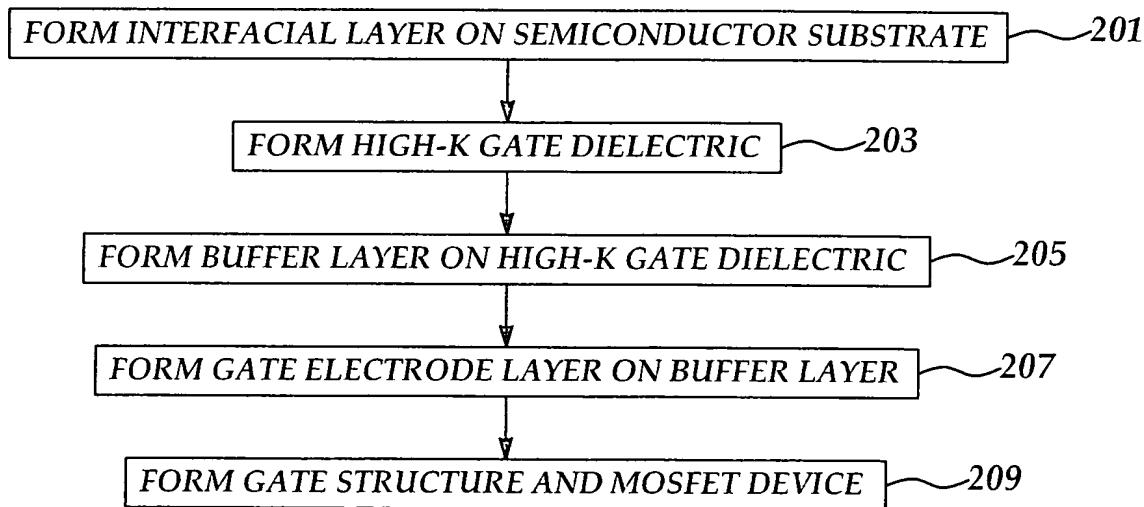


Figure 2